Lab 1 – Feasibility Model Phase 1

ECE 298 – S2021

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| **Team Members** | Team Member 1  Team Member 2 | **Team Number** | XX |

# Part 1 – Project Design Requirements

1. **Functional Requirements**
   1. Requirement
   2. …
2. **Non-functional Requirements**
   1. Requirement
   2. …
3. **Constraint Requirements**
   1. Requirement
   2. …

# Part 2 – Project Considerations for I/O

## **Project Sensors and User Inputs**

{Details}

## Project Actuators and User Outputs

{Details}

## Project MCU Internal Resources

{Details}

# Part 3 – Device Testing Methodology

## Device 1 – PART\_NUMBER

{Details}

## Device 2 – PART\_NUMBER

{Details}

# Part 4 – System-Level Design

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